

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yoshiaki Mori, et al. Art Unit: 1756
Serial No.: 10/026,286 Examiner: Deborah Chacko Davis
Filed: December 20, 2001 Confirmation No.: 1796
Title: Pattern Forming Method and Apparatus Used for Semiconductor
Device, Electric Circuit, Display Module, and Light Emitting Device

RESPONSE D

Sir:

In response to the Office Action dated May 11, 2006 (Paper No./Mail Date 0503), please amend the above-identified application as follows: